



Brennan 2-81-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): J.M. Brennan et al.  
Case: 2-81-1-4  
Serial No.: 10/773,614  
Filing Date: February 6, 2004  
Group: 2823  
Examiner: Brook Kebede

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

Signature: *Shirley L. Vulpis* Date: March 28, 2006

Title: Semiconductor Device Configured for  
Reducing Post-Fabrication Damage

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated December 28, 2005, Applicants submit herewith the following Amendment for entry in the above-identified application:

Entered  
By  
PCB  
5/2/06  
VLS